

FIG.1

FIG.2A

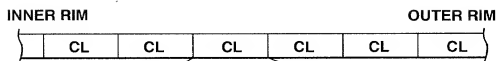


FIG.2B

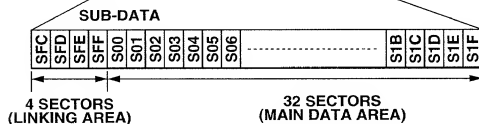


FIG.2C

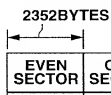


FIG.2D

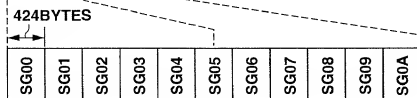
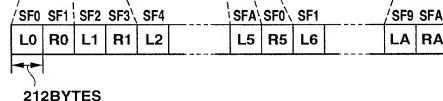


FIG.2E



		16bit				16bit						
		MSB	LSB	MSB	LSB	MSB	LSB	MSB	LSB			
HEADER	{	00000000		11111111		11111111		11111111		0		
		11111111		11111111		11111111		11111111		1		
		11111111		11111111		11111111		00000000		2		
		Cluster · H		Cluster1		Sector		00000010		3		
		00000000		00000000		00000000		00000000		4		
		00000000		00000000		00000000		00000000		5		
		00000000		00000000		00000000		00000000		6		
		Maker code		Model code		First TNO		Last TNO		7		
		00000000		00000000		00000000		Used Sectors		8		
		00000000		00000000		00000000		00000000		9		
ACCOMMODATING TABLE INDICATING DATA WIDTH	{	00000000		00000000		00000000		Disc Serial No		10		
		Disk		ID		P-DFA		P-EMPTY		11		
		P-FRA		P-TNO1		P-TNO2		P-TNO3		12		
		P-TNO4		P-TNO6		P-TNO6		P-TNO7		13		
MANAGEMENT TABLE WIDTH (256 SLOTS)	{									74		
		P-TNO248		P-TNO249		P-TNO250		P-TNO251		75		
		P-TNO252		P-TNO253		P-TNO254		P-TNO255		76		
		00000000		00000000		00000000		00000000		77		
		00000000		00000000		00000000		00000000		78		
		(01h)	START ADDRESS						TRACK MODE		79	
			END ADDRESS						LINK INFORMATION		80	
		(02h)	START ADDRESS						TRACK MODE		81	
			END ADDRESS						LINK INFORMATION		82	
		(03h)	START ADDRESS						TRACK MODE		83	
			END ADDRESS						LINK INFORMATION		84	
		(FCh)	START ADDRESS						TRACK MODE		580	
			END ADDRESS						LINK INFORMATION		581	
		(FDh)	START ADDRESS						TRACK MODE		582	
			END ADDRESS						LINK INFORMATION		583	
(FEh)	START ADDRESS						TRACK MODE		584			
	END ADDRESS						LINK INFORMATION		585			
(FFh)	START ADDRESS						TRACK MODE		586			
	END ADDRESS						LINK INFORMATION		587			

U-TOC SECTOR 0

FIG.3

**FIG.4A**

P - FRA = 

03h
-----

**FIG.4B**

	START ADDRESS	END ADDRESS	LINK INFORMATION
(03h)	S03	E03	18h

**FIG.4C**

(18h)	S18	E18	1Fh
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**FIG.4D**

(1Fh)	S1F	E1F	2Bh
-------	-----	-----	-----

**FIG.4E**

(2Bh)	S2B	E2B	E3H
-------	-----	-----	-----

**FIG.4F**

(E3h)	SE3	EE3	00h
-------	-----	-----	-----

16bit				16bit			
MSB	LSB	MSB	LSB	MSB	LSB	MSB	LSB
00000000	11111111	11111111	11111111	11111111	11111111		
11111111	11111111	11111111	11111111	11111111	11111111		
11111111	11111111	11111111	11111111	00000000	00000000		
Cluster - H	Cluster2	Sector	00000010				
00000000	00000000	00000000	00000000	00000000	00000000		
00000000	00000000	00000000	00000000	00000000	00000000		
00000000	00000000	00000000	00000000	00000000	00000000		
00000000	00000000	00000000	00000000	00000000	00000000		
00000000	00000000	00000000	00000000	00000000	00000000		
00000000	00000000	00000000	00000000	00000000	00000000		
00000000	00000000	00000000	00000000	00000000	00000000		
00000000	00000000	00000000	00000000	00000000	00000000		
00000000	00000000	00000000	00000000	P-EMPTY	P-EMPTY		
00000000	P-TNA1	P-TNO2	P-TNA3				
P-TNA4	P-TNA5	P-TNO6	P-TNA7				

ACCOMMODATING  
TABLE  
INDICATING  
DATA WIDTH

P-TNA248	P-TNA249	P-TNA250	P-TNA251
P-TNA252	P-TNA253	P-TNA254	P-TNA255
DISC NAME			
DISC NAME		LINK INFORMATION	
DISC NAME / TRACK NAME			
DISC NAME / TRACK NAME		LINK INFORMATION	
DISC NAME / TRACK NAME			
DISC NAME / TRACK NAME		LINK INFORMATION	
DISC NAME / TRACK NAME			
DISC NAME / TRACK NAME		LINK INFORMATION	
DISC NAME / TRACK NAME			
DISC NAME / TRACK NAME		LINK INFORMATION	
DISC NAME / TRACK NAME			
DISC NAME / TRACK NAME		LINK INFORMATION	

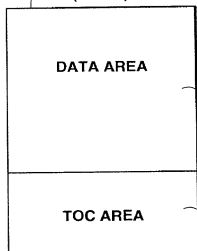
CHARACTER  
TABLE WIDTH

U-TOC SECTOR 1

FIG.5

13 (BUFFER MEMORY)

(DRAM)

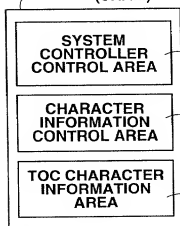


TRANSITION  
STATE 1

FIG.6A

24 (RAM)

(SRAM)



TRANSITION  
STATE 2

FIG.6B

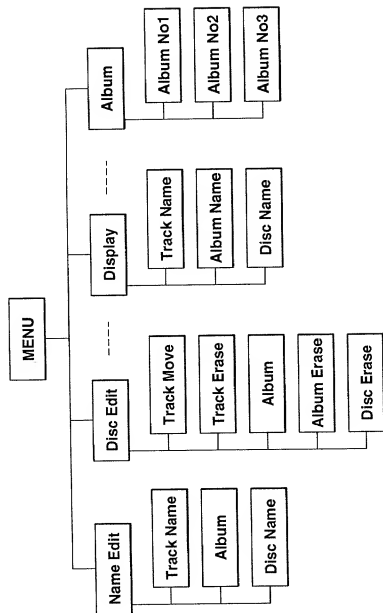


FIG.7

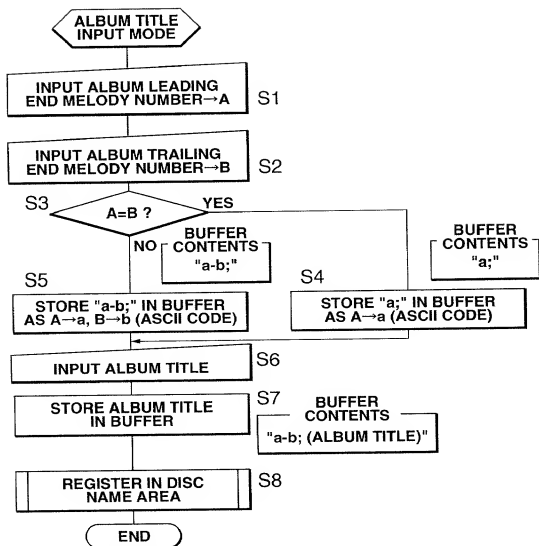


FIG.8

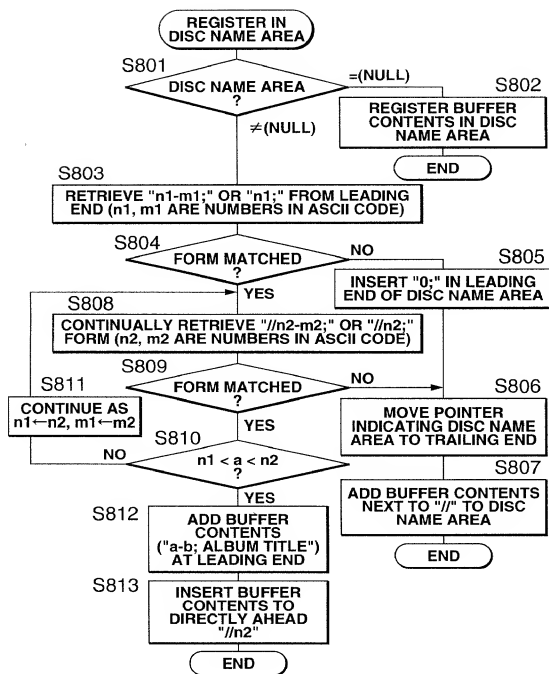


FIG.9

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
. . . .	. . . .	. . . .	. . . .	. . . .
. . . .	. . . .	. . . .	. . . .	. . . .
76	8	—	1	0
77	:	G	A	00
78	00	00	00	00
79	00	00	00	00
80	00	00	00	00
81	00	00	00	00
. . . .	. . . .	. . . .	. . . .	. . . .
. . . .	. . . .	. . . .	. . . .	. . . .

**FIG.10**

FIG.11A

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
76	M	i	n	i
77	D	i	s	01
78	c	00	00	00
79	00	00	00	00
80	00	00	00	00
81	00	00	00	00
...	...	...	...	...

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$



FIG.11B

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	...	...	...	...
76	0	i	M	i
77	n	i	D	01
78	i	s	c	/
79	/	t	-	02
80	7	i	S	O
81	N	Y	00	00
...	...	...	...	...

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
....	....	....	....	....
76	1	—	7	;
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	00
81	00	00	00	00
....	....	....	....	....
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$

FIG.12

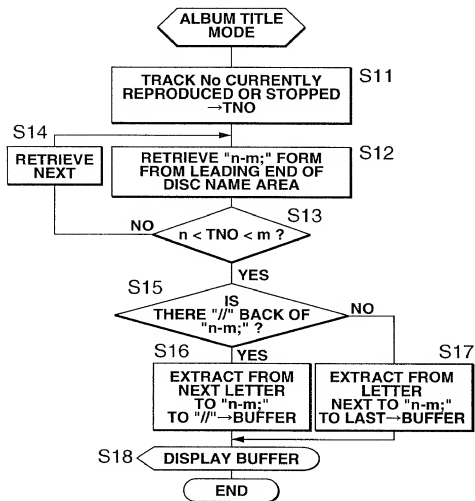
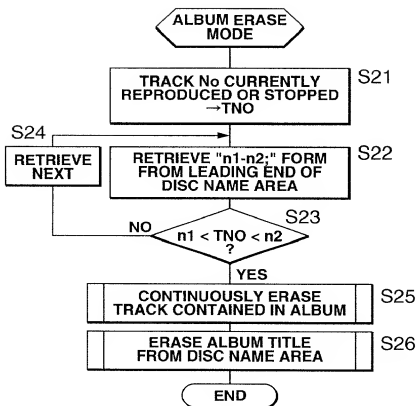


FIG.13



**FIG.14**

FIG.15A

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
....	....	....	....	....
76	1	—	7	;
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	/
81	/	1	1	03
82	—	2	0	;
83	M	i	n	04
84	I	D	i	s
85	c	00	00	00
....	....	....	....	....
....	....	....	....	....

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$



FIG.15B

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
....	....	....	....	....
76	1	—	7	;
77	S	O	N	01
78	Y	/	/	8
79	—	1	7	02
80	;	M	i	n
81	i	D	i	03
82	s	c	00	00
83	00	00	00	00
....	....	....	....	....
....	....	....	....	....

Byte position of the next slot  
 $=76 \times 4 + (\text{Link-P}) \times 8$

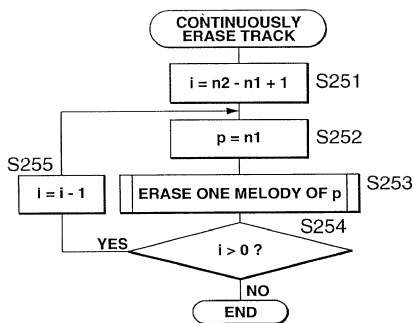


FIG.16

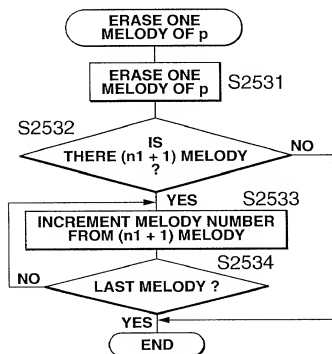
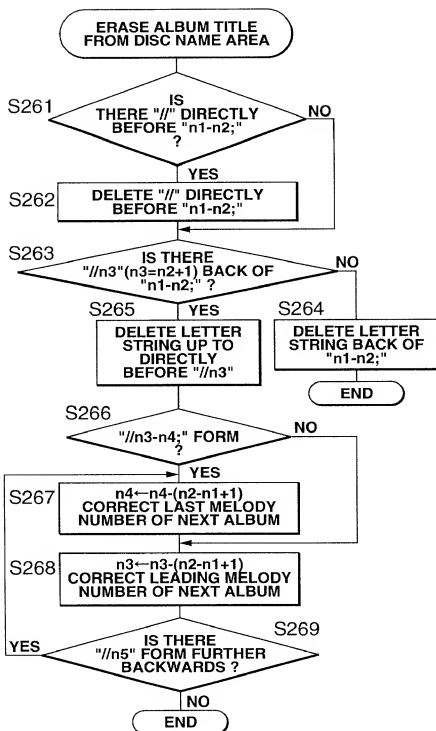
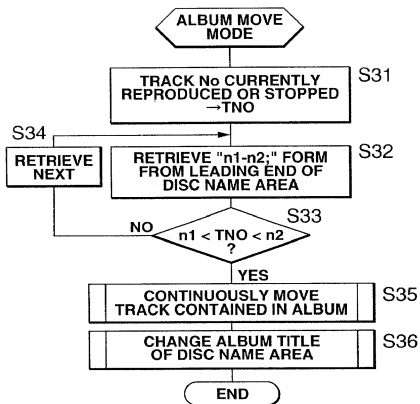


FIG.17



**FIG.18**



**FIG.19**

FIG.20A

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
76	1	---	7	:
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	:	G	A	/
81	/	1	1	03
82	—	2	0	:
83	M	I	n	04
84	I	D	I	s
85	c	00	00	00
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$



FIG.20B

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
76	1	—	7	:
77	S	O	N	01
78	Y	/	/	8
79	—	1	7	02
80	:	M	I	n
81	I	D	/	03
82	s	c	/	:
83	1	8	—	04
84	2	0	:	G
85	A	00	00	00
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$

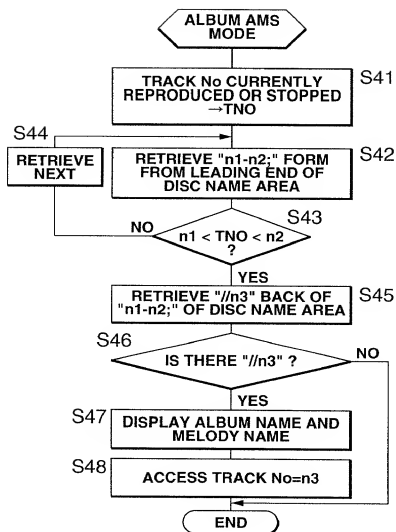


FIG.21

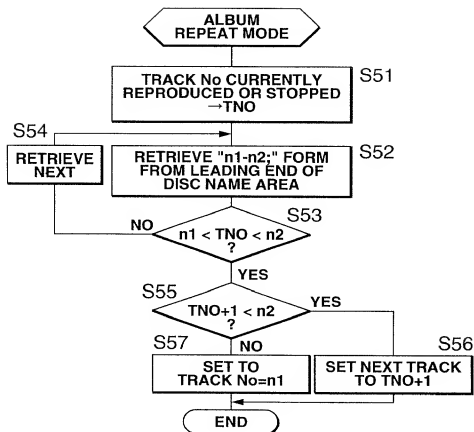
**FIG.22**

FIG.23A

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
76	1	—	7	;
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	/
81	/	1	1	03
82	—	2	0	;
83	M	i	n	04
84	i	D	i	s
85	c	00	00	00
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$



FIG.23B

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
76	1	—	1	0
77	;	S	O	01
78	N	Y	/	/
79	i	1	—	02
80	2	0	;	M
81	i	n	i	03
82	D	i	s	c
83	00	00	00	00
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$

FIG.24A

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
76	1	—	1	0
77	;	S	O	01
78	N	Y	/	/
79	1	1	—	02
80	2	0	;	M
81	i	n	i	03
82	D	i	s	c
83	00	00	00	00
....	....	....	....	....
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$



FIG.24B

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
76	1	—	5	;
77	S	O	N	01
78	Y	/	/	6
79	—	1	0	02
80	;	/	/	1
81	1	—	2	03
82	0	;	M	i
83	n	i	D	04
84	i	s	c	00
85	00	00	00	00
....	....	....	....	....
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$

FIG.25A

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	....	....	....	....
76	1	—	7	;
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	/
81	/	1	1	03
82	—	2	0	;
83	M	i	n	04
84	i	D	i	s
85	c	00	00	00
...	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$



FIG.25B

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	....	....	....	....
76	1	—	6	;
77	S	O	N	01
78	Y	/	/	7
79	—	9	;	02
80	G	A	/	/
81	1	0	—	03
82	1	9	;	M
83	i	n	i	04
84	D	i	s	c
85	00	00	00	00
...	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$

FIG.26A

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
76	1	—	7	;
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	/
81	/	1	1	03
82	—	2	0	;
83	M	i	n	04
84	i	D	i	s
85	c	00	00	00
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$



FIG.26B

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
76	1	—	6	;
77	S	O	N	01
78	Y	/	/	7
79	—	9	;	02
80	G	A	/	/
81	1	0	—	03
82	1	9	;	M
83	i	n	i	04
84	D	i	s	c
85	00	00	00	00
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$

FIG.27A

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
76	1	-	7	:
77	S	O	N	01
78	Y	/	/	8
79	-	1	0	02
80	:	G	A	/
81	/	1	1	03
82	-	2	0	:
83	M	i	n	04
84	i	D	i	s
85	c	00	00	00
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$



FIG.27B

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
76	1	-	8	:
77	S	O	N	01
78	Y	/	/	9
79	-	1	1	02
80	:	G	A	/
81	/	1	2	03
82	-	2	1	:
83	M	i	n	04
84	i	D	i	s
85	c	00	00	00
....	....	....	....	....

Byte position of the next slot  
 $= 76 \times 4 + (\text{Link-P}) \times 8$

FIG.28A

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	....	....	....	....
76	1	—	7	;
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	00
81	00	00	00	00
...	....	....	....	....
...	....	....	....	....

Byte position of the next slot  
 $\equiv 76 \times 4 + (\text{Link-P}) \times 8$



FIG.28B

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
...	....	....	....	....
76	1	—	7	;
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	/
81	/	1	1	03
82	—	2	0	;
83	00	00	00	00
...	....	....	....	....
...	....	....	....	....

Byte position of the next slot  
 $\equiv 76 \times 4 + (\text{Link-P}) \times 8$

FIG.29A

1	2	3	4	5	6	7	8	9	10
---	---	---	---	---	---	---	---	---	----

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
....	....	....	....	....
76	1	—	7	;
77	S	O	N	01
78	Y	/	/	8
79	—	1	0	02
80	;	G	A	00
81	00	00	00	00
....	....	....	....	....
....	....	....	....	....

Byte position  
of the next slot  
= $76 \times 4 + (\text{Link-P}) \times 8$

OVERWRITE  
PORTION

1	2	3	4	5	6	7	8	9	10	11	12	13
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FIG.29B

12	00000000	P-TNA 1	P-TNA 2	P-TNA 3
13	P-TNA 4	P-TNA 5	P-TNA 6	P-TNA 7
14	P-TNA 8	P-TNA 9	P-TNA 10	P-TNA 11
....	....	....	....	....
....	....	....	....	....
76	1	—	3	;
77	S	O	N	01
78	Y	/	/	4
79	—	8	;	02
80	/	/	9	—
81	1	0	;	03
82	/	/	1	1
83	—	1	3	04
84	;	G	A	00
85	00	00	00	00
....	....	....	....	....
....	....	....	....	....

Byte position  
of the next slot  
= $76 \times 4 + (\text{Link-P}) \times 8$